

**Amendments to the Specification:**

Please amend the specification as follows:

Replace paragraph 1 of the "Reference To Related Applications" section, beginning on page 1, lines 13-19, with the following paragraph.

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The present patent application is related to co-pending and commonly owned U.S. Patent Application No. ~~XX/XXX,XXX~~ 10/038,311, Attorney Docket No. POU920010005US1, entitled "Size Reduction Techniques for VITAL Compliant VHDL Simulation Models", now U.S. Patent Application Publication No. US 2003/0125917 A1, and U.S. Patent Application No. ~~XX/XXX,XXX~~ 10/038,689, Attorney Docket No. POU920010166US1, entitled "VHDL Technology Library Method for Efficient Customization of Chip Gate Delay Characteristics", now U.S. Patent Application Publication No. US 2003/0125918 A1, filed on even date with the present patent application, the entire teachings of which being hereby incorporated by reference.